WEST Search History

Hide Items Restore Clear Cancel

DATE: Thursday, May 27, 2004

Hide? Set Name Query Hit Coun				
DB=PGPB, USPT; PLUR=YES; OP=ADJ				
	L35	L34.ti,ab.	7	
	L34	clock near10 ((integrat\$ or combin\$) near3 decod\$)	281	
	L33	clock domain near10 ((integrat\$ or combin\$) near3 decod\$)	1	
	L32	clock domain	1521	
	L31	L30.ti.	0	
	L30	L29 near5 decod\$	334	
	L29	(fifo or buffer) near5 (synchroniz\$ or match\$)	12315	
DB=USPT; PLUR=YES; OP=ADJ				
	L28	L27.ti,ab.	2	
	L27	decod\$ near3 on\$fly	81	
	L26	decod\$ near3 no\$fly	0	
	L25	L22 near5 domain	3	
	L24	L23.ti.	21	
	L23	decod\$ near3 (simultan\$ or concurrent\$ or while)	3822	
	L22	decod\$ near5 (simultan\$ or concurrent\$ or while)	5178	
	L21	L20.ti,ab.	1	
	L20	decod\$ near5 (simultan\$ or concurrent\$ or while) near5 (transfer\$)	82	
	L19	L18 not 112	59	
	L18	fifo same ((integrat\$ or combin\$) near3 decod\$)	73	
	L17	fifo near5 (compound\$ near3 decod\$)	0	
	L16	fifo near5 (compound.\$ near3 decod\$)	0	
	L15	fifo near5 (embed\$ near3 decod\$)	0	
	L14	fifo near5 (embedd\$ near3 decod\$)	0	
	L13	L12 not 13	6	
	L12	fifo near10 ((integrat\$ or combin\$) near3 decod\$)	14	
	L11	L10.ab.	8	
	L10	(fifo or buffer) near5 ((integrat\$ or combin\$) near3 decod\$)	198	
	L9	L7.ti.	15	
	L8	L7.ti,ab.	63	
	L7	(fifo or buffer or memory) near5 ((integrat\$ or combin\$) near3 decod\$)	768	
口	L6	11 near10 decod\$	21	

END OF SEARCH HISTORY

First Hit Fwd Refs

Generate Collection

L3: Entry 1 of 8

File: USPT

Jun 13, 2000

DOCUMENT-IDENTIFIER: US 6076062 A

TITLE: Method and device for transferring and decoding a non-PCM bitstream between a digital video disc and a multi-channel reproduction apparatus

Detailed Description Text (23):

Like the DVD player, MC.sub.-- box 46 has an internal control path 41, interface circuit 38 of type MSM6307, and control processing in microprocessor 40. In correspondence to FIFO 28, the MC.sub.-- box 46 has a relatively small FIFO 44. This stores the data of one bitstream while the previous one is decoded locally. The decoding pertains first to the burst level, and next to the sample level. The output from FIFO 44 feeds MC.sub.-- decoder 42 that may output up to seven audio channels as indicated: Left, Right, LFE/C, Left center surround, Right center surround, Left surround and Right surround. As shown, these are grouped on four I2S interfaces, according to a protocol described in U.S. Pat. No. 4,755,817 to the present assignee. Alternatively FIFO 44 plus decoder 42 are combined into a single hardware block and controlled directly by the commands contained in the IEC 958 data. Moreover, the MC.sub.-- Box attaches to the secondary control channel 48 by means of circuit 38.